

OpenFabrics Alliance

Interoperability Logo Group (OFILG)

July 2015 Logo Report

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Tatyana NikolovaDate:July 22, 2015Intel CorporationReport Revision:1.41300 S. MoPac ExpresswayOFED Version:3.18Austin, TX 78746OS Version:Scientific Linux7.0

Enclosed are the results from OFA Logo testing performed on the following device under test (DUT):

Intel NE020 RNIC

The test suite referenced in this report is available at the UNH-IOL website. Release 2.03 (2015-Jul-16) was used.

http://www.iol.unh.edu/ofatestplan

The following table highlights the Mandatory test results required for the OpenFabrics Interoperability Logo for the DUT per the Test Plan referenced above and the current OpenFabrics Interoperability Logo Program (OFILP).

Test Procedures	IWG Test Status	Result/Notes
12.1: Ethernet Link Initialization	Mandatory	PASS
13.4: TI uDAPL	Mandatory	PASS
13.5: TI RDMA Basic Interoperability	Mandatory	PASS
13.6: TI RDMA Stress	Mandatory	PASS
13.7: TI MPI – Open MPI	Mandatory	PASS

Summary of all results follows on the second page of this report. For specific details regarding issues, please see the corresponding test result.

Testing Completed July 31, 2015

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Reviewed & Issued Oct. 2, 2015

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OFA Logo Report – July 2015 DUT: Intel NE020

Result Summary

The Following table summarizes all results from the event pertinent to this iWARP device class.

Test Procedures	IWG Test Status	Result/Notes
12.1: Ethernet Link Initialization	Mandatory	PASS
<u>TI iSER</u>	Beta	Not Available
TI NFS over RDMA	Beta	Not Supported
13.4: TI uDAPL	Mandatory	PASS
13.5: TI RDMA Basic Interoperability	Mandatory	PASS
13.6: TI RDMA Stress	Mandatory	PASS
13.7: TI MPI – Open MPI	Mandatory	PASS

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Report Revision History

• v1.4 Initial release

Configuration Files

Description	Attachment
Scientific Linux 7.0 Configuration File	9
OFED 3.18 Configuration File	Q

Result Key

The following table contains possible results and their meanings:

Result:	Description:
PASS	The Device Under Test (DUT) was observed to exhibit conformant behavior.
PASS with	The DUT was observed to exhibit conformant behavior however an additional
Comments	explanation of the situation is included.
FAIL	The DUT was observed to exhibit non-conformant behavior.
Qualified PASS	The DUT was observed to exhibit conformant behavior, with the exception of fault(s) or defect(s) which were previously known.
Warning	The DUT was observed to exhibit behavior that is not recommended.
Informative	Results are for informative purposes only and are not judged on a pass or fail basis.
Refer to Comments	From the observations, a valid pass or fail could not be determined. An additional
	explanation of the situation is included.
Not Supported	The DUT does not support the technology required to perform this test.
Not Available	Due to testing station limitations or time limitations, the tests could not be performed.
Borderline	The observed values of the specific parameters are valid at one extreme and invalid at
	the other.
Not Tested	Not tested due to the time constraints of the test period.

DUT and Test Setup Information

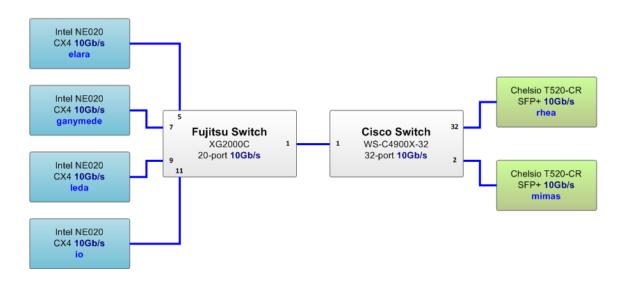
The iWARP fabric configuration utilized for all testing is shown below.

July 2015 Logo iWARP Technology



iWarp Addressing <hostname>-iw.ofa





DUT Details			
Manufacturer:	Intel	Firmware Revision:	3.23
Model:	NE020	Hardware Revision:	N/A
Speed:	10Gb/s	Located in Host:	Elara, Ganymede, Leda, io
Additional Comments / Notes:			

Mandatory Tests - IW Device Test Results:

12.1: Ethernet Link Initialization

Test Result	PASS
Result Discussion:	
All devices were shown to link and pass traffic to all other devices in a back-to-back configuration under	
nominal (unstressed) conditions.	

Link Partner	Chelsio T520-CR	Intel NE020
RNIC: Chelsio T520-CR	N/A	PASS
RNIC: Intel NE020	PASS	N/A

13.4: TI uDAPL

Test Result

Discussion:

All devices were shown to communicate correctly using the Direct Access Programming Library, by use of the Linux dapltest tool.

13.5: TI RDMA Basic Interoperability

Test Result PASS

Discussion:

All devices were shown to correctly exchange core RDMA operations across a simple network path under nominal (unstressed) conditions. Each HCA acted as both a client and a server for all tests.

13.6: TI RDMA Stress

Switch Load	Switch Fan In
PASS	PASS
	0 1110011 = 0 0101

All switches were seen to properly handle a large load as indicated by the successful completion of control communications between two RNICs while other RNICs in the fabric were used to generate traffic in order to put a high load on the switch.

13.7: TI MPI - Open MPI

Test Result	PASS	
Discussion:		
Intel MPI Benchmarks were performed between all HCAs and were observed to exhibit the proper		
behavior.		