

OpenFabrics Alliance Interoperability Working Group (OFA-IWG)

December 2008 Logo Event Report

UNH-IOL – 121 Technology Drive, Suite 2 – Durham, NH 03824 – +1-603-862-0090 OpenFabrics Interoperability Logo Group (OFILG) Hosts – <u>ofalab@iol.unh.edu</u>

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Enclosed are the results from OFA Logo testing performed on the following devices under test (DUTs):

LSI Engenio 0825 (SRP Storage System; SDR) LSI XBB2-IB (Dual Controller SRP Storage System; DDR)

The test suite referenced in this report is available at the OFA website, at test time release 1.22 (August 29, 2008 DRAFT) was used:

http://www.iol.unh.edu/services/testing/ofa/testplan.pdf

The following table highlights the Mandatory test results required for the OpenFabrics Interoperability Logo for the DUT per the testplan referenced above and the current OpenFabrics Interoperability Logo Program (OFILP).

Mandatory Test Procedures	IWG Test Status	Result/Notes
10.1: IB Link Initialization	Mandatory	Passed – no issues seen
10.2: IB Fabric Initialization	Mandatory	Passed – no issues seen
10.3: IB IPoIB Connected Mode	Mandatory	Not applicable to DUT
<u>10.9: TI iSER</u>	Mandatory	Not applicable to DUT
<u>10.10: SRP</u>	Mandatory	Passed – see comments
<u>10.11: SDP</u>	Mandatory	Not applicable to DUT

For specific details regarding issues please see the corresponding test result.

Summary of all results follows on the second page of this report.

Testing Completed 12/23/2008

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Table 1: Result Summary

Test Procedures	IWG Test Status	Result/Notes
10.1: IB Link Initialization	Mandatory	Passed – no issues seen
10.2: IB Fabric Initialization	Mandatory	Passed – no issues seen
10.3: IB IPoIB Connected Mode	Mandatory	Not applicable to DUT
10.4: IB IPoIB Datagram Mode	Beta	Not applicable to DUT
<u>10.9: TI iSER</u>	Mandatory	Not applicable to DUT
<u>10.10: SRP</u>	Mandatory	Passed – see comments
<u>10.11: SDP</u>	Mandatory	Not applicable to DUT
10.12: IB SM Failover and Handover	Beta	Not applicable to DUT
<u>10.13: TI MPI - OSU</u>	Beta	Not applicable to DUT
<u>10.14: TI MPI - Intel</u>	Beta	Not applicable to DUT
<u>10.15: HP MPI - HP</u>	Beta	Not applicable to DUT
<u>10.16: TI MPI - Open</u>	Beta	Not applicable to DUT
<u>10.17: TI uDAPL</u>	Beta	Not applicable to DUT
10.19: IB FibreChannel Gateway	Beta	Not applicable to DUT
10.20: IB Ethernet Gateway	Beta	Not applicable to DUT
10.21: IB Reliable Datagram Sockets	Beta	Not Applicable to DUT
10.22-23: TI Basic RDMA Interoperability	Beta	Not Applicable to DUT
10.24-25: TI RDMA Operations over Interconnect Components	Beta	Not Applicable to DUT

The following table summarizes all results from the event pertinent to an IB device.

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Report Revision History

- v1.0 Initial Release
- v1.01 Retest Results Added
- v1.02 Editorial Changes

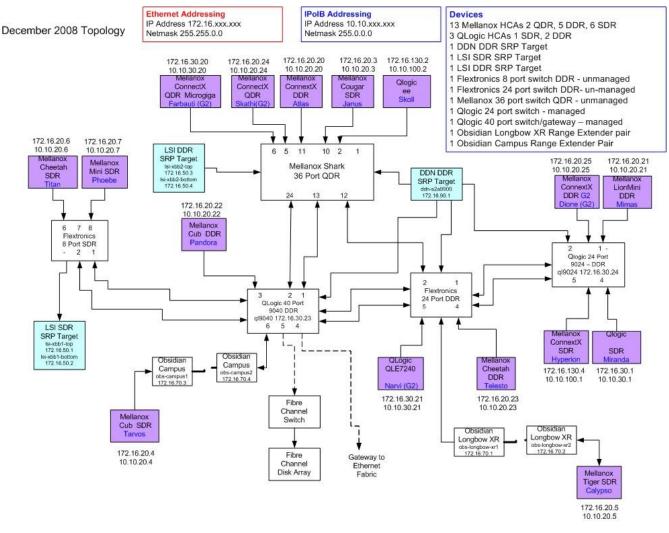
Table 2: Result Key

The following table contains possible results and their meanings:

Result:	Description:
PASS	The Device Under Test (DUT) was observed to exhibit conformant behavior.
PASS with Comments	The DUT was observed to exhibit conformant behavior however an additional explanation of the situation is included, such as due to time limitations only a portion of the testing was performed.
FAIL	The DUT was observed to exhibit non-conformant behavior.
Warning	The DUT was observed to exhibit behavior that is not recommended.
Informative	Results are for informative purposes only and are not judged on a pass or fail basis.
Refer to Comments	From the observations, a valid pass or fail could not be determined. An additional explanation of the situation is included.
Not Applicable	The DUT does not support the technology required to perform this test.
Not Available	Due to testing station limitations or time limitations, the tests could not be performed.
Borderline	The observed values of the specified parameters are valid at one extreme and invalid at the other.
Not Tested	Not tested due to the time constraints of the test period.

Table 3: DUT and Test Setup Information

Figure 1: The IB fabric configuration utilized for any tests requiring a multi-switch configuration is shown below.



DUT #1 Details				
Manufacturer	LSI	Firmware Rev	N/A	
Model	XBB2-IB	Hardware Rev	N/A	
Speed	DDR 4x	IP Address in Fabric	172.16.50.3	
Additional Comments/Notes				
LSI XBB2-IB (Dual Controller SRP Storage System)				

DUT #2 Details				
Manufacturer	LSI	Firmware Rev	N/A	
Model	Engenio 0825	Hardware Rev	N/A	
Speed	SDR 4x	IP Address in Fabric	172.16.50.1	
Additional Comments/Notes				
LSI Engenio 0825 (SRP Storage System)				

Mandatory Tests - IB Device Test Summary Results:

The following tables detail results for tests identified by the OFA-IWG as mandatory tests for the OFA Interoperability Logo Program (OFILP) per the OFA-IWG Interoperability Test Plan Release 1.22 (August 29, 2008)

Test Number and Name	Part(s)	Summary Note(s)	Result(s)		
Group 1: IB Link Initialize	Test #1:	Unable to link to all	PASS		
Discussion: Test #1: Phy link up all ports					
Physical link initialization was varified between this device and every other device in the fabric. Link status was observed					

Physical link initialization was verified between this device and every other device in the fabric. Link status was observed visually via status lights on the device. Port width and link speed was verified via ibdiagnet.

This version of the test plan does not explicitly call for testing of link speed, the next version will. Narvi and Skoll only link at SDR speeds even though they are DDR cards and the XBB2 is a DDR target.

Link Partner Device	XBB2-IB	Engenio 0825
QLogic SilverStorm 9024 (Switch)	PASS	PASS
QLogic SilverStorm 9040 (Switch)	PASS	PASS
Flextronics F-X430066 (Switch)	PASS	PASS
Flextronics F-X430044 (Switch)	PASS	PASS
Mellanox MTS3600 (Switch)	PASS	PASS
Obsidian Longbow-XR (Range Extender)	PASS	PASS
Obsidian Longbow-XR (Range Extender)	PASS	PASS
Obsidian Longbow Campus (Range Extender)	PASS	PASS
Obsidian Longbow Campus (Range Extender)	PASS	PASS
LSI XBB1 (SRP Target)	N/A	N/A
LSI XBB2 (SRP Target)	N/A	N/A
DataDirect Networks (SRP Target)	N/A	N/A
Host: Miranda HCA: QLogic QLE7140 SDR	PASS	PASS
Host: Tarvos HCA: Mellanox LionCub SDR	PASS	PASS
Host: Hyperion HCA: Mellanox Connectx SDR	PASS	PASS
Host: Janus HCA: Mellanox Cougar SDR	PASS	PASS
Host: Phoebe HCA: Mellanox LionMini SDR	PASS	PASS
Host: Titan HCA: Mellanox Cheetah SDR	PASS	PASS
Host: Calypso HCA: Mellanox Tiger SDR	PASS	PASS
Host: Skathi, G2 PCI Express HCA: Mellanox Connectx QDR	PASS	PASS
Host: Farbauti, G2 PCI Express HCA: Mellanox Connectx QDR	PASS	PASS
Host: Narvi, G2 PCI Express HCA: QLogic QLE7280 DDR	PASS with Comments	PASS

Host: Atlas HCA: Mellanox Connectx DDR	PASS	PASS
Host: Telesto HCA: Mellanox Cheetah DDR	PASS	PASS
Host: Dione, G2 PCI Express HCA: Mellanox Connectx DDR	PASS	PASS
Host: Mimas HCA: Mellanox LionMini DDR	PASS	PASS
Host: Skoll HCA: QLogic QLE7240 DDR	PASS with Comments	PASS
Host: Pandora HCA: Mellanox LionCub DDR	PASS	PASS

Test Number and Name	Part(s)	Summary Note(s)	Result(s)		
Group 2: IB Fabric Initialization	Test #1:	Port is Active with all SMs	PASS		
Discussion: Test #1: Verify all SMs configure fabric					

The fabric configuration shown in Figure 1 was used for this test. 'ibdiagnet -c 1000' showed no Port error counters increment. Only one SM is run at a time. All switches are power cycled between SM trials. All links are validated via use of 'ibdiagnet' and 'ibchecknet' was used to verify that there were no duplicate guids. Refer to the table below for SM details.

SMs tested include: *OFED OpenSM (SM Only)*, *QLogic SilverStorm 9024 (Managed Switch)*, *QLogic SilverStorm 9040 (Managed Switch)*

For LSI XBB2-IB (Dual Controller SRP Storage System)	All ports Armed/Active	No Dup GUIDs	No Port errors
OFED OpenSM (SM Only)	PASS	PASS	PASS
QLogic SilverStorm 9024 (Managed Switch)	PASS	PASS	PASS
QLogic SilverStorm 9040 (Managed Switch)	PASS	PASS	PASS

For LSI Engenio 0825 (SRP Storage System)	All ports Armed/Active	No Dup GUIDs	No Port errors
OFED OpenSM (SM Only)	PASS	PASS	PASS
QLogic SilverStorm 9024 (Managed Switch)	PASS	PASS	PASS
QLogic SilverStorm 9040 (Managed Switch)	PASS	PASS	PASS

Test Number and Name	Part(s)	Summary Note(s)	Result(s)
Group 3: IPoIB Connected Mode	Test #1-3	Not Applicable to DUT	Not Applicable
Discussion: Test #1-3			

Test Number and Name	Part(s)	Summary Note(s)	Result(s)
Group 9: TI iSER	Test #1-4	Not Applicable to DUT	Not Applicable
Discussion: Test #1-4			

Test Number and Name	Part(s)	Summary Note(s)	Result(s)
Group 10: IB SRP	Test #1:	Automated Test Script	PASS with Comments
Discussion · Test Result			

The automated test script was revised since the version published in the test document. The automated test script runs the operations in the test plan with every available host and logs the results. The Logs show that all SRP initiators were able to connect and perform the required data transfer operations with the SRP targets while using OpenSM to configure the fabric. The 9024 and the 9040 SM's produced errors with 2 mellanox HCA's during the SRP operations; Titan and Calypso.

These errors can be circumvented by starting either QLogic SM before these HCA's establish link with the fabric. Because of this solution, these devices did not fail. The current test plan does not specify when to start an SM. The next version will.

For LSI XBB2-IB (Dual Controller SRP Storage System)	Verified Automated Test Result
Host: Miranda, with HCA: QLogic QLE7140 SDR	PASS
Host: Tarvos, with HCA: Mellanox LionCub SDR	PASS
Host: Hyperion, with HCA: Mellanox Connectx SDR	PASS
Host: Janus, with HCA: Mellanox Cougar SDR	PASS
Host: Phoebe, with HCA: Mellanox LionMini SDR	PASS
Host: Titan, with HCA: Mellanox Cheetah SDR	PASS with Comments
Host: Calypso, with HCA: Mellanox Tiger SDR	PASS with Comments
Host: Skathi, G2 PCI Express, with HCA: Mellanox Connectx QDR	PASS
Host: Farbauti, G2 PCI Express, with HCA: Mellanox Connectx QDR	PASS
Host: Narvi, G2 PCI Express, with HCA: QLogic QLE7280 DDR	PASS
Host: Atlas, with HCA: Mellanox Connectx DDR	PASS
Host: Telesto, with HCA: Mellanox Cheetah DDR	PASS
Host: Dione, G2 PCI Express, with HCA: Mellanox Connectx DDR	PASS
Host: Mimas, with HCA: Mellanox LionMini DDR	PASS
Host: Skoll, with HCA: QLogic QLE7240 DDR	PASS
Host: Pandora, with HCA: Mellanox LionCub DDR	PASS

For LSI Engenio 0825 (SRP Storage System)	Verified Automated Test Result
Host: Miranda, with HCA: QLogic QLE7140 SDR	PASS
Host: Tarvos, with HCA: Mellanox LionCub SDR	PASS
Host: Hyperion, with HCA: Mellanox Connectx SDR	PASS
Host: Janus, with HCA: Mellanox Cougar SDR	PASS
Host: Phoebe, with HCA: Mellanox LionMini SDR	PASS
Host: Titan, with HCA: Mellanox Cheetah SDR	PASS with Comments
Host: Calypso, with HCA: Mellanox Tiger SDR	PASS with Comments
Host: Skathi, G2 PCI Express, with HCA: Mellanox Connectx QDR	PASS
Host: Farbauti, G2 PCI Express, with HCA: Mellanox Connectx QDR	PASS
Host: Narvi, G2 PCI Express, with HCA: QLogic QLE7280 DDR	PASS
Host: Atlas, with HCA: Mellanox Connectx DDR	PASS
Host: Telesto, with HCA: Mellanox Cheetah DDR	PASS
Host: Dione, G2 PCI Express, with HCA: Mellanox Connectx DDR	PASS

Host: Mimas, with HCA: Mellanox LionMini DDR	PASS
Host: Skoll, with HCA: QLogic QLE7240 DDR	PASS
Host: Pandora, with HCA: Mellanox LionCub DDR	PASS

Test Number and Name	Part(s)	Summary Note(s)	Result(s)
Group 11: TI SDP	Test #1-3	Not Applicable to DUT	Not Applicable
Discussion: Test #1-4			

Beta Tests - IB Device Test Results:

The following table details results for tests identified by the OFA-IWG as beta tests for the OFA Interoperability Logo Program (OFILP) per the OFA-IWG Interoperability Test Plan Release 1.22 (August 29, 2008 DRAFT)

Group 4: IPoIB Datagram ModeTest #1-3Not Applicable to DUTNot ApplicableDiscussion: Test #1-3	Test Number and Name	Part(s)	Summary Note(s)	Result(s)	
Discussion: Test #1-3	Group 4: IPoIB Datagram Mode Test #1-3 Not Applicable to DUT Not Applicable				

Part(s)	Summary Note(s)	Result(s)
Test #1-4	Not applicable to DUT	Not Applicable
1		

Test Number and Name	Part(s)	Summary Note(s)	Result(s)
Group 13: TI MPI – Ohio State Univ.	Test #1-14	Not applicable to DUT	Not Applicable
Discussion: Test Results		-	

Test Number and Name	Part(s)	Summary Note(s)	Result(s)
Group 14: MPI – Intel	Test #1-21	Not applicable to DUT	Not Applicable
Discussion: Test Results			

Test Number and Name	Part(s)	Summary Note(s)	Result(s)
Group 15: MPI – Hewlett-Packard	Test #1-21	Not applicable to DUT	Not Applicable
Discussion: Test Results			

Test Number and Name	Part(s)	Summary Note(s)	Result(s)
Group 16: MPI – Open	Test #1-21	Not applicable to DUT	Not Applicable
Discussion: Test Results		-	

Test Number and Name	Part(s)	Summary Note(s)	Result(s)
Group 17: TI uDAPL	Test #1-10	Not applicable to DUT	Not Applicable
Discussion: Test Results			

Test Number and Name	Part(s)	Summary Note(s)	Result(s)	
Group 19: IB FibreChannel Gateway	Test #1-10	Not applicable to DUT	Not Applicable	
Discussion: Test Results				

OFA Logo Event Report – December 2008 DUT: LSI XBB2-IB (Dual Controller SRP Storage System), LSI Engenio 0825 (SRP Storage System)

Test Number and Name	Part(s)	Summary Note(s)	Result(s)	
Group 20: IB Ethernet Gateway	Test #1-10	Not applicable to DUT	Not Applicable	
Discussion: Test Results				

Test Number and Name	Part(s)	Summary Note(s)	Result(s)	
Group 21: IB Reliable Datagram Sockets	Test #1-10	Not applicable to DUT	Not Applicable	
Discussion: Test Results				

Test Number and Name	Part(s)	Summary Note(s)	Result(s)	
Group 22-23: TI Basic RDMA Interoperability	Test #1-10	Not applicable to DUT	Not Applicable	
Discussion: Test Results				

Test Number and Name	Part(s)	Summary Note(s)	Result(s)	
Group 24-25: TI RDMA Operations over Interconnect Components	Test #1-10	Not applicable to DUT	Not Applicable	
Discussion: Test Results				